

Electronic Acknowledgement Receipt

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Title of Invention:	Method and apparatus for defect analysis of semiconductor integrated circuit
First Named Inventor/Applicant Name:	Masahiro Ishida
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
1	Amendment After Final	transmittal.pdf	36578 e4adc6eb387f57b852d99200a7c8eb441ba40436	no	2

Warnings:

Information:

2	Claims	claims.pdf	117994 168dd327fd2d6015d1a2622a3d378c35e5b d1ee9	no	4
Warnings:					
Information:					
3	Applicant Arguments/Remarks Made in an Amendment	remarks.pdf	152446 693472276bfb271b22d9f0b6494a78cb7d3 48fc1	no	6
Warnings:					
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Total Files Size (in bytes):				307018	
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